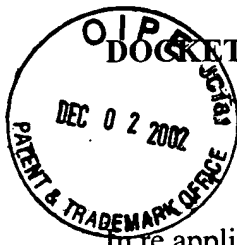


2123



DOCKET NO. KAPUR 5-10

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sharad Kapur, *et al.*

Serial No.: 09/427,238

Filed: October 26, 1999

For: SYSTEM AND METHOD FOR DETERMINING CAPACITANCE FOR
LARGE-SCALE INTEGRATED CIRCUITS

Group No.: 2123

Examiner: Eduardo Garcia-Otero

Commissioner for Patents
Washington, D. C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on
11/26/2002 (Date)
EDITH SHEIC
(Printed or typed name of person signing the certificate)
Edith Sheic
(Signature of the person signing the certificate)

Sir:

AMENDMENT UNDER 37 C.F.R. § 1.111

The Applicants have carefully considered this application in connection with the Examiner's Action mailed on August 27, 2002, and respectfully request reconsideration of this application in view of the following amendment and remarks.